

Refine Search

Search Results -

Term	Documents
VIRTUAL\$4	0
VIRTUAL	170762
VIRTUALA	1
VIRTUALAB	2
VIRTUALAGE	2
VIRTUALALDB	1
VIRTUALAN	2
VIRTUALAND	1
VIRTUALAY	8
VIRTUALBASE	1
VIRTUALBEAM	1
(VIRTUAL\$4 AND (L14 OR L15 OR L16 OR L17)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	32

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Database:

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 IBM Technical Disclosure Bulletins

Search:

L24

Refine Search

Recall Text

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Search History

DATE: Thursday, November 04, 2004 [Printable Copy](#) [Create Case](#)

Set Name **Query**
 side by side

Hit Count **Set Name**
 result set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L24</u>	virtual\$4 and (l14 or l15 or l16 or l17)	32	<u>L24</u>
<u>L23</u>	virtual\$4 and (l14 or l15 or l16 or l17)	3983	<u>L23</u>
<u>L22</u>	virtual\$4 and l21	11	<u>L22</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L21</u>	L20 and (convert\$5 or conversion\$1 or translat\$5 or encod\$3 or decod\$3) and microcontroller\$1	36	<u>L21</u>
<u>L20</u>	(716/16)[CCLS]	441	<u>L20</u>
<u>L19</u>	(716/16)![CCLS]	441	<u>L19</u>
<u>L18</u>	(716/16)[CCLS]	441	<u>L18</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L17</u>	L13 and l7	12	<u>L17</u>
<u>L16</u>	L13 and l6	18	<u>L16</u>
<u>L15</u>	L13 and l4	25	<u>L15</u>
<u>L14</u>	L13 and l3	29	<u>L14</u>
<u>L13</u>	L12 and (convert\$5 or conversion\$1 or translat\$5 or encod\$3 or decod\$3) and microcontroller\$1	313	<u>L13</u>
<u>L12</u>	(field near1 programmable near1 gate or fpga or pld or plm or mcm) near10 (random\$3 near2 access\$5 or ram\$1 or dram\$1 or sram\$1)	2734	<u>L12</u>
<u>L11</u>	l5 and l7	6	<u>L11</u>
<u>L10</u>	l5 and l6	13	<u>L10</u>
<u>L9</u>	l5 and l4	23	<u>L9</u>
<u>L8</u>	l5 and l3	27	<u>L8</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L7</u>	(711/1-173)![CCLS]	19934	<u>L7</u>
<u>L6</u>	(710/2-317)[CCLS]	17811	<u>L6</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L5</u>	(convert\$5 or conversion\$1 or translat\$5 or encod\$3 or decod\$3) and l1 and microcontroller\$1	263	<u>L5</u>
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DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L4</u>	(712/2-300)[CCLS]	10415	<u>L4</u>
<u>L3</u>	(716/2-21)![CCLS]	6935	<u>L3</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L2</u>	L1 near25 microcontroller\$1	67	<u>L2</u>
<u>L1</u>	(field near1 programmable near1 gate or fpga) near10 (random\$3 near2 access\$5 or ram\$1 or dram\$1 or sram\$1)	1899	<u>L1</u>

END OF SEARCH HISTORY